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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : Benyong Zhang
Application No. : 10/694,450
Filed : October 27, 2003
For : METHOD AND SYSTEM FOR PROVIDING A PHASE-
LOCKED LOOP WITH REDUCED SPURIOUS TONES
Group No. : 2816
Examiner : Minh T. Nguyen

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents and publications herein are listed below and on the attached Forms PTO/SB/08A & B. Copies of the listed publications are submitted herewith.

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
3,928,813	Kingsford-Smith	December 23, 1975
4,204,174	King	May 20, 1980
4,206,425	Nossen	June 3, 1980

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<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
4,609,881	Wells	September 2, 1986
4,815,018	Reinhardt et al.	March 21, 1989
5,038,120	Wheatley et al.	August 6, 1991
5,124,670	Lawton	June 23, 1992
5,920,233	Denny	July 6, 1999

Publications

Tadao Nakagawa et al., "A Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses a New Pulse Generator LSI", IEEE Transactions on Microwave Theory and Techniques, Vol. 42, No. 12, December 1994, Pp. 2579-2582.

Tom A. D. Riley et al., "Delta-Sigma Modulation in Fractional- N Frequency Synthesis", IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1993, Pp. 553-559.

Brian Miller et al., "A Multiple Modulator Fractional Divider" Forty-Fourth Annual Symposium on Frequency Control, IEEE, 1990, Pp. 559-568.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

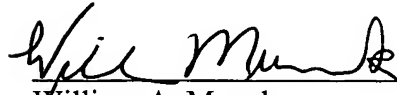
This Information Disclosure Statement is being transmitted after the mailing date of the first Office Action on the merits. Therefore, Applicant encloses a check in the amount of \$180.00 for the Information Disclosure Statement filing fee.

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APPLICATION NO.: 10/694,450
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Respectfully submitted,

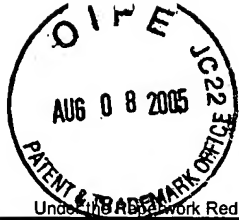
DAVIS MUNCK, P.C.

Date: Aug. 3, 2005



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PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1

of 2

Complete if Known

Application Number	10/694,450
Filing Date	October 27, 2003
First Named Inventor	Benyong Zhang
Art Unit	2816
Examiner Name	Minh T. Nguyen
Attorney Docket Number	P05719

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	AA	US- 3,928,813	12/23/1975	Kingsford-Smith	
	AB	US- 4,204,174	05/20/1980	King	
	AC	US- 4,206,425	06/03/1980	Nossen	
	AD	US- 4,609,881	09/02/1986	Wells	
	AE	US- 4,815,018	03/21/1989	Reinhardt et al.	
	AF	US- 5,038,120	08/06/1991	Wheatley et al.	
	AG	US- 5,124,670	06/23/1992	Lawton	
	AH	US- 5,920,233	07/06/1999	Denny	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Attorney Docket Number	P05719

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BA	Tadao Nakagawa et al., "A Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses a New Pulse Generator LSI", IEEE Transactions on Microwave Theory and Techniques, Vol. 42, No. 12, December 1994, Pp. 2579-2582.	
	BB	Tom A. D. Riley et al., "Delta-Sigma Modulation in Fractional-N Frequency Synthesis", IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1993, Pp. 553-559.	
	BC	Brian Miller et al., "A Multiple Modulator Fractional Divider" Forty-Fourth Annual Symposium on Frequency Control, IEEE, 1990, Pp. 559-568.	

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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